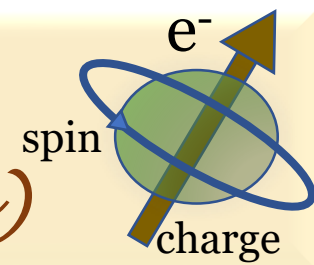




W2S Seminar

(Webinar series on Spintronics)



SOT-MRAM: basics, technological implementation and perspectives



Speaker:
Prof. Kevin GARELLO
Spintec, CEA Grenoble, France

Date and time:
03.06.2022 at
7.00 pm IST
i.e. 3.30 pm CET

Abstract

I will cover the different challenges to take up SOT-MRAM from material and stack optimization to technology large-scale integration and circuit design. I will describe typical full-scale integration process of perpendicular magnetized SOT-MRAM devices on 300mm wafers, using manufacturable field free methods and CMOS compatible processes. Finally, I will discuss progress challenges in material development, as well as cell and architecture design required to bring SOT-MRAM toward industrial maturity. I will conclude on some opportunities to diversify its application spectra, notably in the field of machine learning.

To attend the lecture please visit: **Passcode: 375617** Zoom link:
<https://us06web.zoom.us/j/89242546981?pwd=TURFcVRRQmZrMGJaSGI5OUc0OUUrdz09>

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